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McDermott, Will & Emery

Date Mailed: 09/17/2001

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Applicant(s)

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Title

LSI design method and verification method

Preliminary Class

Applicant: Kentaro SHIZUKI, et al.

Docket No. 60188-031

TitleMark: 1.5: DESIGN METHOD AND VERIFICATION METHOD

Serial/Reg./Patent No. _____

Date Sent: February 9,

☒ Transmittal Letter ☒ Hand Carried ☐ Fax ☐ 1st-Class Mail ☐ Cert. of Mailing ☐ Express Mail No. _____

New Patent App ☒ Utility ☐ Design ☐ Cont. ☐ CIP ☐ Div. ☐ PCT ☐ CPA ☐ RCE ☐ Prov

Other: _____ ☐ Trademark/Service Mark/Intent-To-Use Application ☐ Amend ment to Alleged Use ☐ Statement of Use

13 pages of Specification

5 pages of Claims

1 pages of Abstract

16 pages of Formal/Informal Drawings

☐ Information Disclosure Statement

Form PTO 1449

copies of cited references

☐ Preliminary Amendment

☐ Response to Missing Parts Notice

☐ Certified Copy of Priority Doc

☒ Claim for Convention Priority

☐ Verified Statement of Small Entity Status

☐ Transmittal of Issue Fee.

☐ Maintenance Fee for _____ years after grant

☐ Notice of Appeal ☐ Appeal Brief

☐ Recordation of _____ Patent _____ Trademark:

☐ Response/Amendment to Office Action of _____

☐ Other _____

Assignments/Security Agreements/

☐ Petition to Commissioner for _____

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